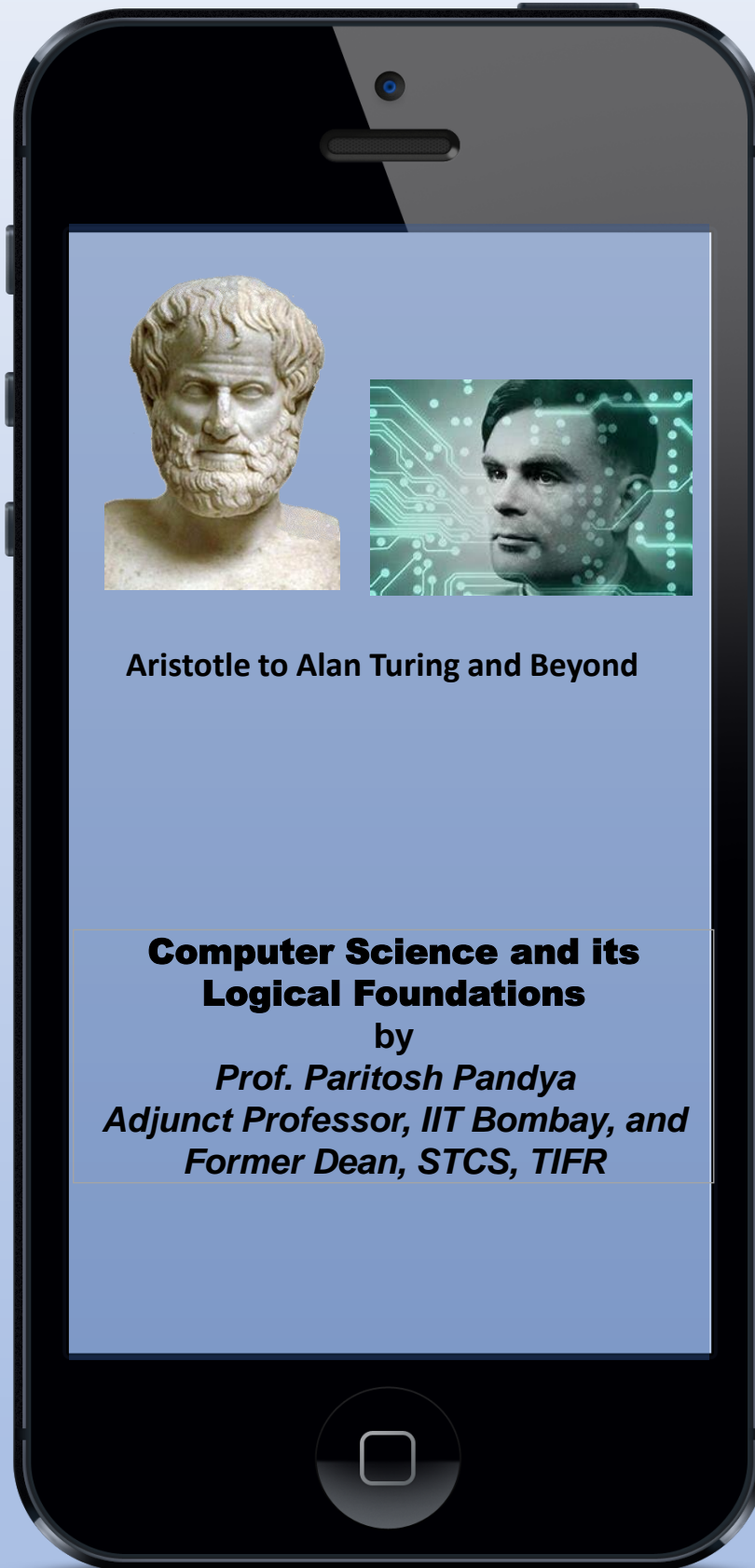




NATIONAL SCIENCE DAY LECTURE

*Paritosh Pandya is a computer scientist whose areas of interest include logic, automata, mathematics of program construction and embedded systems. He is the co-recipient of the IEEE TCRTS Test of Time award in its inaugural year 2020. He is the former Dean of STCS, TIFR where he worked as a researcher for over 33 years. He has also worked as a research officer in Oxford University Computing Laboratory under Prof C.A.R. Hoare and as a visiting scientist at the United Nation University's International Institute of Software Technology. Paritosh Pandya loves Indian classical music and he is trying to teach the same to his computer.*



Logic is often called the "Calculus" of Computer Science. Logic arose as a broad-spectrum language for rule-based reasoning. Hilbert proposed doing mathematics axiomatically using logic. We will recount the fascinating story of how logicians evolved the idea of computing. Building upon this, we will explore how diverse logical systems form a basis for articulating and analyzing computational phenomena. Conversely, computer algorithms can be used to mechanically perform logical reasoning. The talk will explore the deep seated connections between logic and computer science.

February 28, 2023 at 11 a.m.

Lecture Theatre AG 66, TIFR, Homi Bhabha Road, Mumbai 400005



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