## A S E T Forum of TIFR



Overview of National Supercomputing Mission (NSM) and Emerging trends in architectures of HPC systems

Mr. Sanjay Wandhekar Senior Director and HOD, HPC Technologies, C-DAC

The talk will give an overview of Supercomputing systems and indigenous developments under the National Supercomputing Mission being implemented by C-DAC and supported by DST and MeitY. Typical architecture of a HPC system will be presented. The current trends in various elements of an exascale HPC systems like Compute servers, CPUs, GPUs, HPC Network, storage, software environments, etc. shall be discussed. Also if time permits Data centre architecture required for such a large Exascale system will be highlighted.



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Mr. Sanjay Wandhekar is Senior Director at C-DAC, India, and heading the HPC Technologies group. He has 30+ years of management experience in the development & design of HPC and IPTV/OTT Technologies and systems. His education qualifications include a B.E. in Electronics & Telecommunication from the College of Engineering, Pune, and later an M. Tech from IIT, Kanpur.

Sanjay is currently heading the National Supercomputing Mission (NSM) activities at C-DAC, which include the establishment of Supercomputing facilities & ecosystem development in the country and the development of critical HPC technologies like compute servers, high-speed HPC interconnect and system software stack. In last 10 years, his team has established more than 30 Supercomputing facilities in the country. He and his team has received Dr. A P J Abdul Kalam HPC Award for their contribution to Supercomputing ecosystem in the country.

Sanjay has proven track record of building, mentoring, and motivating high-performance teams at C-DAC and in the industry. He has comprehensive experience in all phases of product development ranging from concept to production to services. He was the lead member in the development of India's first Supercomputer PARAM8000 and the subsequent PARAM series of supercomputers.



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